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Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450
Attention: L. Cruz
Art Unit: 2827

Re: Y. YAMAGUCHI, et al. – Application No.: 09/811,401
Attorney Docket No.: 500.39919X00

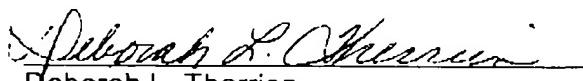
SUBMISSION OF AMENDMENT
AND PETITION FOR EXTENSION OF TIME

Sir:

Applicant hereby submits the attached "Amendment After Final Rejection" (11 pgs) and an "Explanation with drawings" (1 p)," in the above-identified application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached "Amendment After Final Rejection" (11 pgs), and an "Explanation with drawings" (1 p) are being formally filed in the USPTO via Group 2800 Facsimile No. 703-872-9319 on May 13, 2003.


 Deborah L. Therrien

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500.39919X00
Response Under 37 CFR 1.116
Expedited Procedure
Group No. 2827

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. YAMAGUCHI, et al
Application No.: 09/811,401
Filed: March 20, 2001
For: SEMICONDUCTOR DEVICE AND METHOD FOR
MANUFACTURING THE SAME AND SEMICONDUCTOR
DEVICE-MOUNTED STRUCTURE
Art Unit: 2827
Examiner: L. Cruz

AMENDMENT AFTER FINAL REJECTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 13, 2003

Sir:

In response to the Final Office Action dated February 13, 2003, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claim 2 as follows:

2. (Twice Amended) A semiconductor device according to Claim 1, wherein a protuberant portion is formed in a surrounding part connected to the inclined edge portion of the stress relaxation layer, said protuberant portion having a height which is slightly higher than a height of a flat portion of the stress relaxation layer, and a deflected portion is formed in the wiring existing on said protuberant portion.